

Epson RX6110 SPI Linux Driver - February 3, 2014\_K3.8-v1.2

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Added second interrupt (IRQ2)

Epson RX6110 SPI Linux Driver - January 22, 2014\_K3.8-v1.1

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Added register read/write and voltage low detection read/clear via ioctl.

Epson RX6110 SPI Linux Driver - January 9, 2014\_K3.8-v 1.0

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The RX6110 SPI Linux device driver provides the means for an application running in user space to access the Epson RX6110 RTC. Note: The RX6110 supports both SPI and I2C interfaces, if you are configured for I2C, download the RX6110 I2C driver.

The SPI driver can be easily modified according to the user's requirements and rebuilt. The driver was tested using Linux kernel 3.8.x and was developed using the ARMhf Ubuntu 12.04LTS distribution on a BeagleBone Black. It is expected that changes and additions will be required for driver implementation on other platforms/interfaces based on the specific requirements of those platforms.

### Installing the Driver

To install the driver source:

1. Copy the file rtc-rx6110-spi.c into the directory ./linux-3.8.x/drivers/rtc. "linux-3.8.x" refers to the base of the linux kernel source tree.

2. Add the following lines into the drivers/rtc/Kconfig file:

```
config RTC_DRV_RX6110_SPI
```

```
    tristate "Epson RX6110SA SPI"
```

```
    help
```

```
    If you say yes here you get support for the Epson
    RX6110SA SPI RTC chip.
```

```

    This driver can also be built as a module. If so, the module
    will be called rtc-rx6110-spi.
```

3. Add the following line to the drivers/rtc/Makefile:

```
obj-$(CONFIG_RTC_DRV_RX6110_SPI) += rtc-rx6110-spi.o
```

4. During the rebuild, make sure to include the 'Epson RX6110SA SPI' option in the Kernel Configuration window under 'Device Drivers' -> 'Real Time Clock'.

## Hardware Considerations

The testing of the driver was done using an Epson RX6110 part mounted on a breadboard connected to the BeagleBone Black P9 header. The connections are as follows:

RX6110 pin	BeagleBone Black P9	Notes
-----	-----	-----
GND	P9_01	
VDD	P9_03	
/IRQ1	P9_23	/* gpio1_17: used for IRQ1 */
/IRQ2	P9_27	/* gpio3_19: used for IRQ2 */
CE/FOE	P9_28	/* spi: spi1_cs0 */
DI/SDA	P9_29	/* spi: spi1_d0 */
DO/FOUT	P9_30	/* spi: spi1_d1 */
CLK/SCL	P9_31	/* spi: spi1_sclk */

Note: Both IRQs need to have pull-up resistors.

## Testing Environment

The following assumes that the RX6110 driver has been modified as required, rebuilt, and included in the linux build as either a built-in driver or a module.

Also BB-BONELT-HDMI overlay needs to be disabled from loading to avoid the conflict as it uses same SPI. To do it add the following line into boot/uEnv.txt file on SD card:

```
optargs=quiet capemgr.disable_partno=BB-BONELT-HDMI
```

To test the driver, the "RTC breadboard" was setup as a cape on the BeagleBone Black header P9. This requires adding the RX6110 RTC into the device tree using a device tree overlay (.dtbo). A sample device tree source file (.dts) and the compiled .dtbo file are included in the driver package. These files must be copied to the /lib/firmware directory on the BeagleBone Black system.

Before loading the cape you have to enable GPIO modules. Please see note (at the end of this document) to create the bbb\_enable\_gpio.sh script to work around this problem.

Once the system has booted type the following (you may need root access depending on your setup/configuration):

```
./bbb_enable_gpio.sh
echo BBB-RX6110-SPI > /sys/devices/bone_capemgr.*/slots
cat /sys/devices/bone_capemgr.*/slots
```

The RX6110 cape should now be listed in one of the slots and the driver should be loaded at /dev/rtc1 (again depending on your configuration). To confirm that the driver has loaded, type:

```
dmesg | grep rx6110
```

To test the read/write capabilities of the RX6110, we can now use the hwclock command. To get the current time, type:

```
hwclock -r -f /dev/rtc1
```

If the time has not been set yet, the rtc should return with Jan 1, 1970. Next, we can set the system date with the following example:

```
date -s "Thurs Nov 07 11:33:00 PDT 2013"
```

Write the new date to the clock:

```
hwclock -w -f /dev/rtc1
```

And read it back:

```
hwclock -r -f /dev/rtc1
```

The RX6110 is now set with the new time.

The driver package also includes small sample applications, rtctest.c and iocctl.c.

The rtctest demonstrates how to read and write the time from/to the RX6110 using the iocctl's RTC\_RD\_TIME and RTC\_SET\_TIME.

The iocctl demonstrates how to read and write to the registers of RX6110.

For instance, to read register (10h):

```
./iocctl read 10
```

```
REG[10h]=>29h
```

to write a value (20h) to register (10h):

```
./iocctl write 10 20
```

```
REG[10h]<=20h
```

to read Voltage Low Flag bit status:

```
./iocctl vlr
```

```
0
```

to clear Voltage Low Flag bit status:

```
./iocctl vlc
```

## NOTE:

To create bbb\_enable\_gpio.sh script copy and paste to your BeagleBone Black terminal the following lines and then: `chmod +x bbb_enable_gpio.sh`

```
echo '#!/bin/bash
```

```
#https://github.com/piranha32/IOoo/blob/master/examples/bbb_enable_gpio.sh
```

```
#workaround for a bug in bone-pinmux-helper to enable GPIO modules.
```

```
#based on the code from Luigi Rinaldi:
```

```
#https://groups.google.com/forum/!msg/beagleboard/OYFp4EXawil/Mq6s3sg14HoJ
```

```
EXPORT=/sys/class/gpio/export
```

```
echo 5 > /sys/class/gpio/export
```

```
echo 65 > /sys/class/gpio/export
```

```
echo 105 > /sys/class/gpio/export' >> bbb_enable_gpio.sh
```