

SEIKO EPSON CORPORATION
Sales & Marketing Division.
Device Sales & Marketing Dept.
MICRODEVICES Operations Division.
TD Production Management Control Dept.

SUB: EOL notice of SPXO SG-310S*F

Dear Valued Customer:

Thank you for choosing SEIKO EPSON CORP. Timing Products as your products of choice. SEIKO EPSON CORP. always tries to improve the characteristics and performance of our products. However, we regret to announce the following products end of life. The Last time buy, and the Last shipment schedule are following:
We appreciate if you will confirm the final number of orders for these products.

1. Products Affected, Recommend Alternatives

Product Name	P/N	Recommended Alternative	
SG-310SEF SG-310SDF SG-310SCF	Q33310FE0xxxx xx Q33310FD0xxxx xx Q33310F70xxxx xx	Standard frequency *See below standard frequency table	SG3225CAN
		Frequencies other than standard	SG-8018CE SG-8101CE *See below Note for Pin 1 terminal function

At the same time, we also announced the maintenance of SG-310S*N, but we notified the end of production and sales in document No. 20-027.

Please note that the final order has been closed by the end of August 2021.

2. EOL Schedule

Last time Buy	Last shipment
Sep/28 2022	End of Mar 2023

<Standard Frequency list>

STD Frequency (MHz)							
1	4.000000	6	14.745600	11	25.000000	16	33.333300
2	8.000000	7	16.000000	12	26.000000	17	40.000000
3	10.000000	8	20.000000	13	27.000000	18	48.000000
4	12.000000	9	24.000000	14	32.000000	19	50.000000
5	12.288000	10	24.576000	15	33.330000	20	72.000000

Please contact your area EPSON device sales office, if you have any concern of this EOL notice.

Sincerely yours,

Notes for #1 pin function

	SG-210 Series SG-310 Series SG5032CAN SG7050CAN		SG-8101 series SG-8018 series	
#1 pin function	Standby Function (\overline{ST})		Standby Function (\overline{ST})	
	#1 status	Function	#1 status	Function
	High or Open	Enable output	High only	Enable output
	Low	Standby	Low	Standby

SG-8101 and SG-8018 series #1 pin is not designed for “Open” status use.
Please apply “High” level voltage to #1 pin when enable output.