

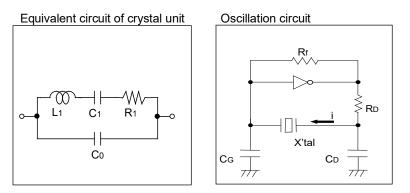
PRECAUTIONS IN DESIGNING OSCILLATION CIRCUITS

1. Drive level

Drive level denotes electric power required to oscillate a crystal unit, which can be calculated using the following formula.

Drive level (P) = $i^2 \cdot Re$

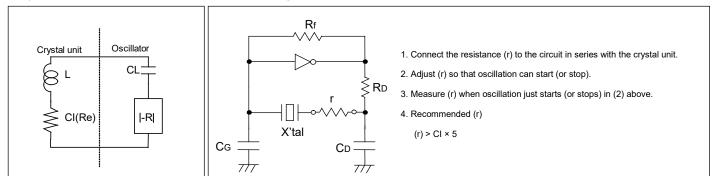
Where i stands for current to pass in the crystal unit, Re for effective resistance of crystal unit, and $Re=R_1(1+Co/CL)^2$. If the Drive level (P) exceeds the specified level, oscillation frequency will shift. This occurs because an excessive level of power causes stress for the crystal and, consequently, temperature rises. If excessive drive level of power is applied to the crystal unit, this may deteriorate or damage the characteristics.



2. Allowance for oscillation

Unless adequate negative resistance is allocated in the oscillation circuit, oscillation start-up time may be increased, or No oscillation may occur. In order to avoid this, provide enough negative resistance in the circuitry design.

- Crystal unit and Oscillator
- Check of Negative resistance



3. Load capacitance

Differences in the load capacitance of the oscillation circuit may result in a different oscillation frequency from the desired one, as shown in the figure below. Approximate expression of the load capacitance of the circuit $CL = CG \times CD / (CG+CD)+CS$. Where CS stands for stray Capacity of the circuit.

